

AMENDMENTS TO THE SPECIFICATION

Please amend the following paragraphs:

[0051] At this time, the EM sensor includes a first transparent insulating layer over the first coil array including the second substrate, wherein the first coil is formed on the second substrate; and a second transparent insulating layer over the first transparent insulating layer, including the second coil array, wherein the second coil array is formed on the first transparent insulating layer ~~a first transparent insulating layer on the insulating layer, wherein the first coil array is formed between the first transparent insulating layer and the insulating layer; and a second transparent insulating layer on the first transparent insulating layer, wherein the second coil array is formed between the first transparent insulating layer and the second transparent insulating layer.~~

[0062] At this time, the EM sensor includes a first transparent insulating layer over the color filter layer including the the first coil array, wherein the first coil array is formed on the color filter; and a second transparent insulating layer over the first transparent insulating layer including the second coil array, wherein the second coil array is formed on the first transparent insulating layer ~~a first transparent insulating layer on the overcoat layer, wherein the first coil array is formed between the first transparent insulating layer and the overcoat layer; and a second transparent insulating layer on the first transparent insulating layer, wherein the second coil array is formed between the first transparent insulating layer and the second transparent insulating layer.~~

[0073] At this time, the EM sensor includes a first transparent insulating layer over the insulating layer including the first coil array, wherein the first coil array is formed on the insulating layer; and a second transparent insulating layer over the first transparent insulating layer including the second coil array, wherein the second coil array is formed on the first transparent insulating layer ~~a first transparent insulating layer on the insulating layer, wherein the first coil array is formed between the first transparent insulating layer and the insulating layer; and a second transparent insulating layer on the first transparent insulating layer, wherein the second coil array is formed between the first transparent insulating layer and the second transparent insulating layer.~~

[0128] Hereinafter, an LCD device including an EM type touch panel according to the present invention, in which an EM sensor 300 is formed on an inner surface of an upper substrate 51 or a lower substrate 52 in an LCD panel 500 [[400]].

[0140] Still referring to FIG. 15, an EM sensor including an X-axis transparent electrode coil array and a Y-axis transparent electrode coil array is formed on an upper substrate 51, and then a light-shielding layer 71 is formed above the EM sensor 300 for shielding portions except the pixel region from light. Then, a color filter layer 72 is formed corresponding to at least the pixel electrode 63, and an overcoat layer 73 is formed on an entire surface of the upper substrate 51 including the color filter layer 72 for flattening the surface of the upper substrate 51. Subsequently, a common electrode 74 is formed on an entire surface of the overcoat layer 73. At this time, the overcoat layer 73 is formed of an organic insulating layer having low dielectric constant, such as PhotoAcryl, BenzoCycloButen BCB or Polyamide compound, for preventing electric interference from the EM sensor 300.